**brŒKiw bghJ¤ nj®Î kh®¢ - 2021**

**tF¥ò – 12**

**Ïa‰Ãaš**

neu« : 2.30 hrs. kÂ¥bg© : 15

mfkÂ¥ÕL : 05, òwkÂ¥ÕL : 15

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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| EXPT  NO | FORMULA  (WITH  UNITS) | EXPANSION OF FORMULA | DIAGRAM | PROCEDURE | TABULAR  COLUMN  WITH  READINGS | CALCULATION | GRAPH | RESULT  (CORRECT  ANSWER-1/2  UNIT – ½) |
| 1 | 1 | 1 | 1 ½ | 1 ½ | 5 | 4 | - | 1 |
| 2 | 1 | 1 | 1 ½ | 1 ½ | 5 | 4 | - | 1 |
| 3 | 1 | 1 | 1 ½ | 1 ½ | 5 | 2 | 2 | 1 |
| 4 | 1 | 1 | 1 ½ | 1 ½ | 6 | 3 | - | 1 |
| 5 | 1 | 1 | 1 ½ | 1 ½ | 6 | 3 | - | 1 |

1. nlŠr‹£ fhštdhÛ£liu¥ ga‹gL¤Â òéfhªj¥òy¤Â‹ »il¤js¡T¿‹ kÂ¥ig f©l¿f (Fiwªjg£r« 4 msÅLfshtJ njit)
2. ä‹dG¤jkhåia¥ ga‹gL¤Â, bfhL¡f¥g£LŸs ä‹fy‹fë‹ ä‹åa¡F éiria x¥ÃLf. (Fiwªjg£r« 5 msÅLfshtJ njit)
3. PN rªÂ ilnaho‹ (V-I) g©ò tiunfhLfis tiuªJ K‹ndh¡F¢ rh®ò tiunfhLfëš ÏUªJ K‹ndh¡F¢rh®ò ä‹jil k‰W« tisÎ¥ òŸë ä‹dG¤j ntWgh£il f©LÃo¡fÎ«.
4. bjhF¥ò¢R‰W¡fis¥ ga‹gL¤Â OR, AND, NOT NAND k‰W« NOR M»a j®¡f thæšfë‹ c©ik m£ltizfis¢ rç¥gh®¡fÎ«.
5. O kh®fâ‹ Kjš k‰W« Ïu©lhtJ nj‰w§fis¢ rçgh®¡fÎ«.

PRATICAL PUBLIC EXAMINATION MARCH 2021

STD - XII

TIME : 2.30 hrs MARK : 15

PHYSICS

INTERNAL : 05 , EXTERNAL : 15

|  |  |  |  |  |  |  |  |  |
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| EXPT  NO | FORMULA  (WITH  UNITS) | EXPANSION OF FORMULA | DIAGRAM | PROCEDURE | TABULAR  COLUMN  WITH  READINGS | CALCULATION | GRAPH | RESULT  (CORRECT  ANSWER-1/2  UNIT – ½) |
| 1 | 1 | 1 | 1 ½ | 1 ½ | 5 | 4 | - | 1 |
| 2 | 1 | 1 | 1 ½ | 1 ½ | 5 | 4 | - | 1 |
| 3 | 1 | 1 | 1 ½ | 1 ½ | 5 | 2 | 2 | 1 |
| 4 | 1 | 1 | 1 ½ | 1 ½ | 6 | 3 | - | 1 |
| 5 | 1 | 1 | 1 ½ | 1 ½ | 6 | 3 | - | 1 |

1. Determine the value of the Horizontal component of the Earth’s magnetic field (Bh) using Tangent Galvanometer ( Take at least four readings)
2. Compare the emf of two cells using potentiometer. ( Take at least five readings)
3. Draw the V-I characteristics of PN junction diode and determine its forward resistance and knee voltage from forward characteristics.
4. Verify the truth table of logic gates OR, AND, NOT NAND and NOR gates using integrated circuits.
5. Verification of De-Morgan’s Theorems using integrated circuits.